IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Seiji Ohno et al	: Art Unit:	2826
Serial No.:	09/937,194	: Examiner:	Johannes P. Mondt
Filed:	September 21, 2001	:	
For:	LIGHT-EMITTING THYRISTOR MATRIX ARRAY AND DRIVER CIRCUIT	:	

AMENDMENT AND REQUEST FOR EXTENSION OF TIME

Assistant Commissioner for Patents
Washington, DC 20231

S I R :

Responsive to the Official Action dated **May 8, 2002**, please amend the above-identified application as follows:

SPECIFICATION:

Please replace the paragraph, at page 3, lines 8-21, with the following amended paragraph:

Using the light-emitting thyristor matrix array, the number of bonding pads on a matrix array chip may be decreased. The number M of the gate-selecting lines in this structure in which the number of bonding pads may be decreased is selected in such a manner that M is an integer near to $N^{1/2}$ and N/M is an integer. For example, when M=8 or M=16 is selected in case of N=128, the number of bonding pads is 24 and this is minimum value. Therefore, it is possible to make the chip area small, resulting in the decrease of the chip cost. The circuit structure in Fig. 1 using light-emitting thyristors has been proposed by the present applicant, and Japanese Patent has already been issued (Japanese Patent No. 2807910).